



## CONNECTOR LEGEND

### CONNECTOR J1

<b>V+</b>	12VDC @ 250mA
<b>COM</b>	Power Supply and Signal Common
<b>CD-</b>	Signal Input USITT DMX512/AMX192 Data/Clock Complement
<b>CD+</b>	Signal Input USITT DMX512/AMX192 Data/Clock True

### CONNECTOR J2

Analog Control Outputs  
 Low-level 0 - +2v  
 High-level +7 - +15V  
 Max. Drive 10mA

### CONNECTOR J3

<b>PIN</b>	Aux. Control Outputs on 14 pin DIL header
<b>1-12</b>	Channels 1 to 12
<b>13 &amp; 14</b>	Signal Common

## ADDRESS SELECTION

Three rotary switches select the offset start address for the unit in most configurations. In test mode, the switches set dimmers to full one at a time. The switches are set as (S4) hundreds, (S3) tens and (S2) ones.

## DIP Switch Settings

<b>STATUS QUO HOLD TIME</b>	<b>S1-1</b>
Disabled (2 Sec. timeout)	OFF
Enabled ( 5 Min. timeout)	ON
Maintains last dimmer levels for set time on loss of input data signal	
<b>TEST AND CALIBRATE MODE</b>	<b>S1-2</b>
Disabled (Normal Operation)	OFF
Enabled	ON
When enabled allows analog dimmer outputs to be brought to full one at a time as selected by the rotary address switches.	
<b>FUTURE USE</b>	<b>S1-3</b>
<b>DMX TERMINATION</b>	<b>S1-4</b>
DMX Line Unterminated	OFF
DMX Line Terminated	ON
This switch connects a 100-ohm resistor across the DMX data pair. The unit should be terminated if it is the last receiving device on the DMX line.	

## LED INDICATORS

Two LEDs are used to indicate power supply and processor run status and data receive detection.

**RUN** Glowing steadily indicates power supply and processor OK; off indicates no power, and flashing indicates defective processor hardware.

**RxD** Glowing steadily indicates data signal received; off indicates no signal present. Note that an address selection out of the range of the data signal will extinguish the LED.

## OUTPUT CONFIGURATION

**CAUTION – Disconnect power from the unit before making any changes**

For 4-12V output, JP2 is **installed**.

For 12-15V output, JP2 is **removed**.

JP1 is for factory use only and should not be installed.

## OUTPUT VOLTAGE ADJUSTMENT

Place the unit in test mode (S1-2 on) and set the address switches to 000 or 001. Ensure that the decoder is connected to dimmer no. 1 and JP3 is in the correct position. Connect a DC voltmeter between COM and output terminal no. 1 on the circuit board. Adjust P1 to achieve the desired high-level full on control voltage, and adjust P2 for the low-level full off voltage.

**NOTE:** The LCDM12 is factory preset for low-level 0V, high-level 10V output



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