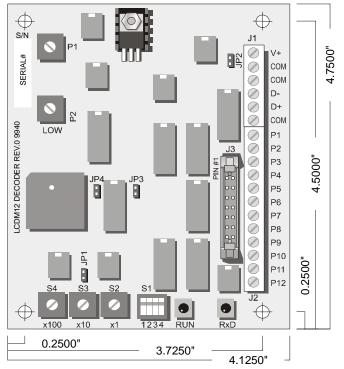
# \_CDM12 DEMULTIPLEXER



### LED INDICATORS

Two LEDs are used to indicate power supply and processor run status and data receive detection.

- **RUN** Glowing steadily indicates power supply and processor OK; off indicates no power, and flashing indicates defective processor hardware.
- **RxD** Glowing steadily indicates data signal received; off indicates no signal present. Note that an address selection out of the range of the data signal will extinguish the LED.

### OUTPUT CONFIGURATION

## CAUTION – Disconnect power from the unit before making any changes

For 4-12V output, JP2 is **installed**. For 12-15V output, JP2 is **removed**.

JP1 is for factory use only and should not be installed.

### OUTPUT VOLTAGE ADJUSTMENT

Place the unit in test mode (S1-2 on) and set the address switches to 000 or 001. Ensure that the decoder is connected to dimmer no. 1 and JP3 is in the correct position. Connect a DC voltmeter between COM and output terminal no. 1 on the circuit board. Adjust P1 to achieve the desired high-level full on control voltage, and adjust P2 for the low-level full off voltage.

#### CONNECTOR LEGEND

	CONNECTOR J1		
V+	12VDC @ 250mA		
СОМ	Power Supply and Signal Common		
CD-	Signal Input USITT DMX512/AMX192 Data/Clock Complement		
CD+	Signal Input USITT DMX512/AMX192 Data/Clock True		
	CONNECTOR J2		
	Analog Control Outputs Low-level 0 - +2v High-level +7 - +15V Max. Drive 10mA		
	CONNECTOR J3		
PIN	Aux. Control Outputs on 14 pin DIL header		
1-12	Channels 1 to 12		
13 & 14	Signal Common		
ADDRE	SS SELECTION		
in most to full or	tary switches select the offset start address for t configurations. In test mode, the switches set di ne at a time. The switches are set as (S4) hur s and (S2) ones.	mmers	
DIP Sw	ritch Settings		
STATUS	S QUO HOLD TIME	S1-1	
<b>D:</b>		0.55	

Configuration

Disabled (2 Sec. timeout)			
Enabled ( 5 Min. timeout)			
Maintains last dimmer levels for set time on loss of input data signal			
TEST AND CALIBRATE MODE			
Disabled (Normal Operation)			
Enabled			
When enabled allows analog dimmer outputs to be brought to full one at a time as selected by the rotary address switches.			
FUTURE USE	S1-3		
DMX TERMINATION			
DMX Line Unterminated			
DMX Line Terminated			

This switch connects a 100-ohm resistor across the DMX data pair. The unit should be terminated if it is the last receiving device on the DMX line.

**NOTE:** The LCDM12 is factory preset for low-level 0V, high-level 10V output



Pathway Connectivity Inc., 480C - 36 Avenue S.E., Calgary, AB, T2G 1W4 Canada tel (403) 243-8110 fax (403) 287-1281 support@pathwayconnect.com www.pathwayconnect.com

