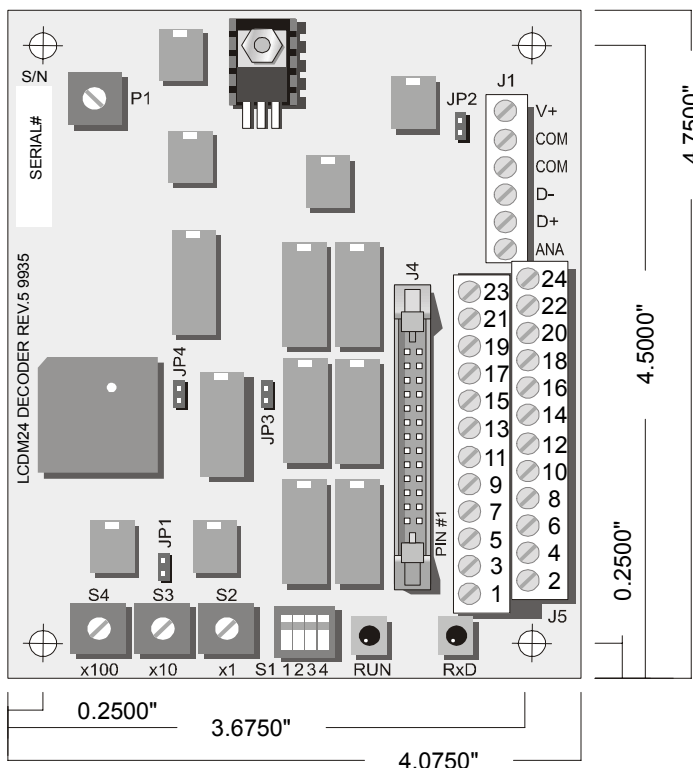


LCDM24 DEMULTIPLEXER

Configuration



CONNECTOR LEGEND

CONNECTOR J1

V+	12VDC @ 250mA
COM	Power Supply and Signal Common
COM	Power Supply and Signal Common
CD-	Signal Input USITT DMX512/AMX192 Data/Clock Complement
CD+	Signal Input USITT DMX512/AMX192 Data/Clock True
ANA	USITT AMX192 or D54 Analog Input (optional)

CONNECTOR J5

Analog Control Outputs
Min. 0 to +10V Max. 0 to +15V
Max. Drive 1mA

CONNECTOR J4

PIN	Aux. Control Outputs on 26 pin DIL header
1-24	Channels 1-24
25 & 26	Signal Common

PINOUT FOR DB25 CONNECTORS

1-12	Channels 1-12
13	Common
14-25	Channels 13-24

ADDRESS SELECTION

Three rotary switches select the offset start address for the unit in most configurations. In test mode, the switches set dimmers to full one at a time. The switches are set as (S4) hundreds, (S3) tens and (S2) ones.

DIP Switch Settings

STATUS QUO HOLD TIME	S1-1
Disabled (2 Sec. timeout)	OFF
Enabled (5 Min. timeout)	ON
Maintains last dimmer levels for set time on loss of input data signal	
TEST AND CALIBRATE MODE	S1-2
Disabled (Normal Operation)	OFF
Enabled	ON
When enabled allows analog dimmer outputs to be brought to full one at a time as selected by the rotary address switches.	
FUTURE USE	S1-3
DMX TERMINATION	S1-4
DMX Line Unterminated	OFF
DMX Line Terminated	ON
This switch connects a 100-ohm resistor across the DMX data pair. The unit should be terminated if it is the last receiving device on the DMX line, otherwise leave this switch OFF.	

LED INDICATORS

Two LEDs are used to indicate power supply and processor run status and data receive detection.

RUN Glowing steadily indicates power supply and processor OK; off indicates no power, and flashing indicates defective processor hardware.

RxD Glowing steadily indicates data signal received; off indicates no signal present. Note that an address selection out of the range of the data signal will extinguish the LED.

OUTPUT CONFIGURATION

CAUTION – Disconnect power from the unit before making any changes

For 4-12V output **install** JP2.

For 12-15V output **remove** JP2.

If J4 is used with a DB25 output connector, install JP1 to reassign the analog output signals as shown in the Connector Legend chart, otherwise **DO NOT** install JP1

OUTPUT VOLTAGE ADJUSTMENT

Place the unit in test mode (S1-2 ON) and set the address switches to 000 or 001. Ensure that the decoder is connected to dimmer no. 1 and JP1 & JP2 are in the correct position. Connect a DC voltmeter between COM and output terminal no. 1 on the circuit board. Adjust P1 to achieve the desired full on control voltage.

GRAY

Pathway Connectivity, 480C - 36 Avenue S.E.,
Calgary, AB, T2G 1W4 Canada
tel (403) 243-8110 fax (403) 287-1281

support@pathwayconnect.com
www.pathwayconnect.com.com

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