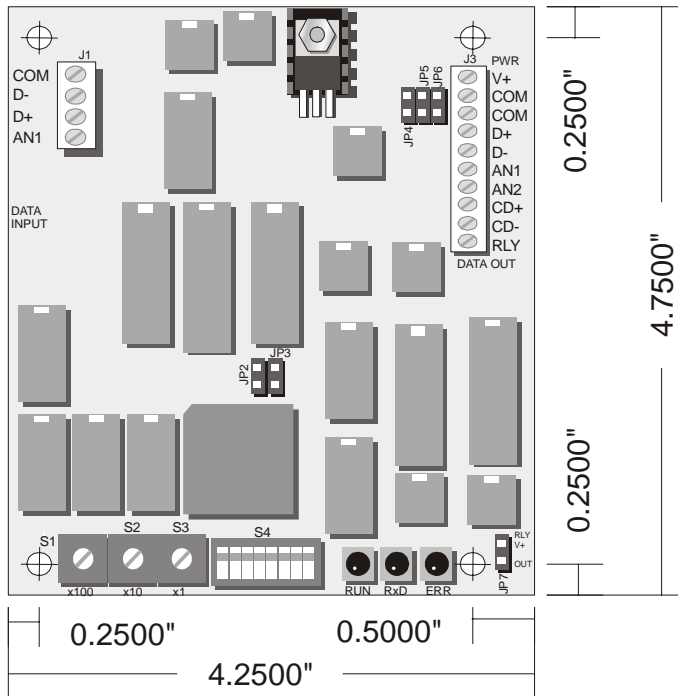


LCPC2K PROTOCOL CONVERTER

Configuration



DIP SWITCH SETTINGS

I/O MODE SELECT	1	2	3	4	5
AMX-DMX	ON	ON	ON	ON	ON
DMX-AMX	OFF	ON	ON	ON	ON
DMX-DUAL AMX	ON	OFF	ON	ON	ON
D54 - DMX	OFF	OFF	ON	ON	ON
DMX-D54	ON	ON	OFF	ON	ON
AMX-AMX	OFF	ON	OFF	OFF	OFF
DMX-DMX	OFF	OFF	OFF	OFF	OFF
D54-DUAL AMX	ON	OFF	OFF	OFF	OFF
AMX-ECMUX	OFF	ON	OFF	ON	ON
ECMUX-AMX	ON	OFF	OFF	ON	ON
DMX-ECMUX	OFF	OFF	OFF	ON	ON
ECMUX-DMX	ON	ON	ON	OFF	ON
D54-ECMUX	OFF	ON	ON	OFF	ON
ECMUX-D54	ON	OFF	ON	OFF	ON
AMX-AVAB	OFF	OFF	ON	OFF	ON
AVAB-AMX	ON	ON	OFF	OFF	ON
DMX-AVAB	OFF	ON	OFF	OFF	ON
AVAB-DMX	ON	OFF	OFF	OFF	ON
D54-AVAB	OFF	OFF	OFF	OFF	ON
AVAB-D54	ON	ON	ON	ON	OFF
AMX-CMX	OFF	ON	ON	ON	OFF
CMX-AMX	ON	OFF	ON	ON	OFF
DMX-CMX	OFF	OFF	ON	ON	OFF
CMX-DMX	ON	ON	OFF	ON	OFF
D54-CMX	OFF	ON	OFF	ON	OFF
CMX-D54	ON	OFF	OFF	ON	OFF
AMX-K96	OFF	OFF	OFF	ON	OFF
K96-AMX	ON	ON	ON	OFF	OFF
DMX-K96	OFF	ON	ON	OFF	OFF
K96-DMX	ON	OFF	ON	OFF	OFF
D54-K96	OFF	OFF	ON	OFF	OFF
K96-D54	ON	ON	OFF	OFF	OFF

CONNECTOR DESIGNATION

CONNECTOR J1		CONNECTOR J3	
COM	INPUT SIGNAL COMMON	V+	+9 to +12VDC @150mA
D1-	DMX1 DATA- / AMX CLOCK- INPUT	COM	POWER SUPPLY COMMON
D1+	DMX1 DATA+ / AMX CLOCK+ INPUT	COM	OUTPUT SIGNAL COMMON
AN1	AMX/D54 ANALOG INPUT	D+	DMX DATA+ / AMX1 CLOCK+ OUTPUT
JUMPERS		D-	DMX DATA- / AMX1 CLOCK- OUTPUT
JP1	FACTORY USE	AN1	AMX1/D54 ANALOG OUTPUT
JP2-3	FACTORY OPTION USE	AN2	AMX2 ANALOG OUTPUT
JP4-5	Shunt for DMX OUTPUT Open for AMX OUTPUT	CD+	AMX2 CLOCK+ OUTPUT
JP6	Shunt for D54 OUTPUT Open for AMX1 OUTPUT	CD-	AMX2 CLOCK- OUTPUT
JP7	RELAY DRIVE OUTPUT	RLY	RELAY DRIVE OUTPUT Open collector output used with V+ to drive external relays/LEDs

LED INDICATORS

Three LEDs are used to indicate power supply, processor run status and data receive detection.

RUN: Glowing steadily indicates power supply and processor OK; off indicates no power, and flashing indicates defective processor hardware.

RXD: Glowing steadily indicates data signal received; off indicates no signal present.

ERR: Normal condition is off; glowing, indicates incorrect setting of DIP switches or address switches.

ADDRESS SELECTION

Three rotary switches select the offset start address for the unit in most configurations. For dual AMX output, the address switches select the starting dimmer number for the second AMX output line. In test mode, the switches set dimmers to full one at a time. The switches are set as (S1) hundreds, (S2) tens and (S3) ones.

FILTERED OUTPUT

ENABLED	ON
DISABLED	OFF

This function applies a 2-bit (1%) filter to the output signal to prevent device fluctuation.

STATUS QUO HOLD TIME

ENABLED (5 MIN. TIME OUT)	ON
DISABLED (2 SEC. TIMEOUT)	OFF

Maintains last dimmer settings for a set time on loss of input data signal

RUN/TEST MODE SELECT

TEST MODE	ON
RUN MODE	OFF

OUTPUT TEST: The selected output protocol is transmitted when dimmer number selected by address switches is set to full on.

INPUT TEST: The selected input protocol is analyzed, and L2 indicates correct or in-spec signal (glowing steadily), incorrect or out-of-spec signal (flashing) or no signal (off)



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